

# Advance Program

## 20AM1 : Keynote Speech 1

Date: August 20, 9:45-10:35  
Room: Forum Hall, Shikoku University Plaza, Shikoku University  
Chair: XXXX XXXX (XXXX XXXX)

- [20AM1-1](#) **Thermal/Traffic-aware 3D Network-on-Chip (NoC) Designs** 1  
An-Yeu Wu (National Taiwan University)

## 20AM2 : Keynote Speech 2

Date: August 20, 10:40-11:30  
Room: Forum Hall, Shikoku University Plaza, Shikoku University  
Chair: XXXX XXXX (XXXX XXXX)

- [20AM2-1](#) **Designs of high-frequency and high-efficiency amplifiers -Applications of computation algorithm of bifurcation analyses to amplifier designs-** 2  
Hiroo Sekiya (Chiba University)

## 20PM1-1 : Power and Energy Circuits and Systems

Date: August 20, 13:00-14:12  
Room: Seminar Room 1, Shikoku University Plaza, Shikoku University  
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- [20PM1-1-1](#) **Analysis of Class-E<sup>2</sup> Wireless Power Transfer System for Any System Parameter** 3  
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- [20PM1-1-2](#) **A Comprehensive Design Procedure of Inductively Coupled Wireless Power Transfer System for Maximizing Overall Efficiency** 4  
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- [20PM1-1-4](#) **Automatic Synthesis Platform for Low Dropout Regulator** 6  
Ling-Yen Song (National Central University), Hao-Yu Chi (National Central University), Hsing-Ju Chang (National Central

University), Chien-Nan Liu (National Central University)

## 20PM1-2 : Signal and Image Processing

Date: August 20, 13:00-14:12

Room: Seminar Room 2, Shikoku University Plaza, Shikoku University

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Date: August 20, 15:15-16:27

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## 21AM1-1 : VLSI Systems and Applications

Date: August 21, 09:00-10:12

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